

**DDR2 DRAM functional testing during power on self test**, diagnostic testing, or as part of manufacturing test may require the DRAM to operate at clock rates much slower than the minimum specified frequency of 125 Mhz. For example, a manufacturing functional test using boundary scan in the controller to test multiple DDR2 components on a board may require the clock to operate at a 5 Mhz. rate.

To learn more about how to configure and operate Qimonda DDR2 DRAM at low clock rates please send an email request to: [NSM.Application.Support@Qimonda.com](mailto:NSM.Application.Support@Qimonda.com)